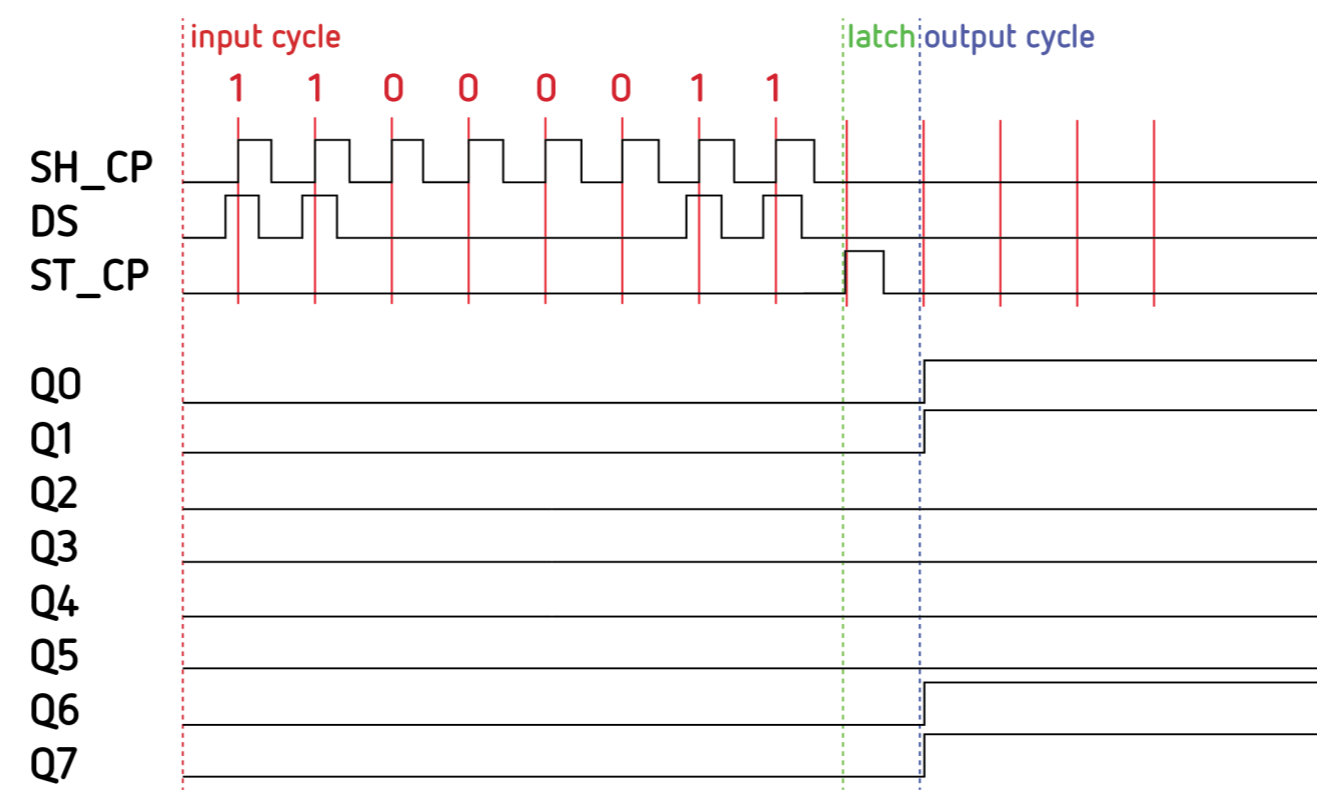
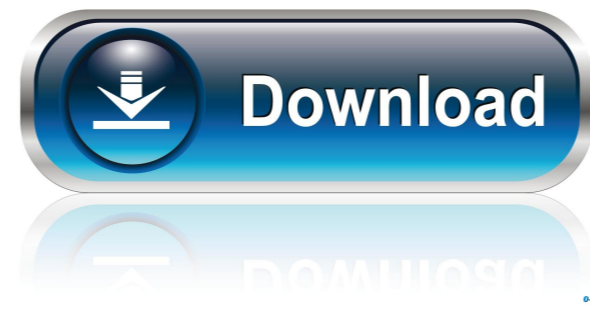


Verilog Code For 8 Bit Parallel In Serial Out Shift 31



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